



8FW 2133

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Manuel J. Alvarez II; Thomas A. Dye; Peter D. Geiger
Assignee: Quickshift, Inc.
Title: System And Method For Recognizing And Configuring Devices
Embedded On Memory Modules
Serial No.: 09/840,724 Filing Date: 4/23/01
Examiner: Shelly A. Chase Group Art Unit: 2133
Docket No.: 40532-P008US Conf. No.: 4539
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Dallas, Texas
March 14, 2005

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION DATED 12/13/04

I. INTRODUCTORY COMMENTS

Dear Sir:

This paper is responsive to the Non-Final Office Action dated December 13, 2004, having a shortened statutory period expiring on March 13, 2005 (extended to March 14, 2005 because March 13, 2005 was a Sunday).

Further examination is respectfully requested in view of the remarks set forth below.

Amendments begin on page 2 of this paper.

Remarks begin on page 18 of this paper.

Conclusion begins on page 19 of this paper.